



Reducing the reliance on verification experts for seL4 proofs



faster + cheaper =
better

sometimes you only need to pick two

How we plan to become faster and cheaper



- ▶ Formal verification is great, but
 - availability of experts still a bottleneck
 - development time still hard to scale



How we plan to become faster and cheaper



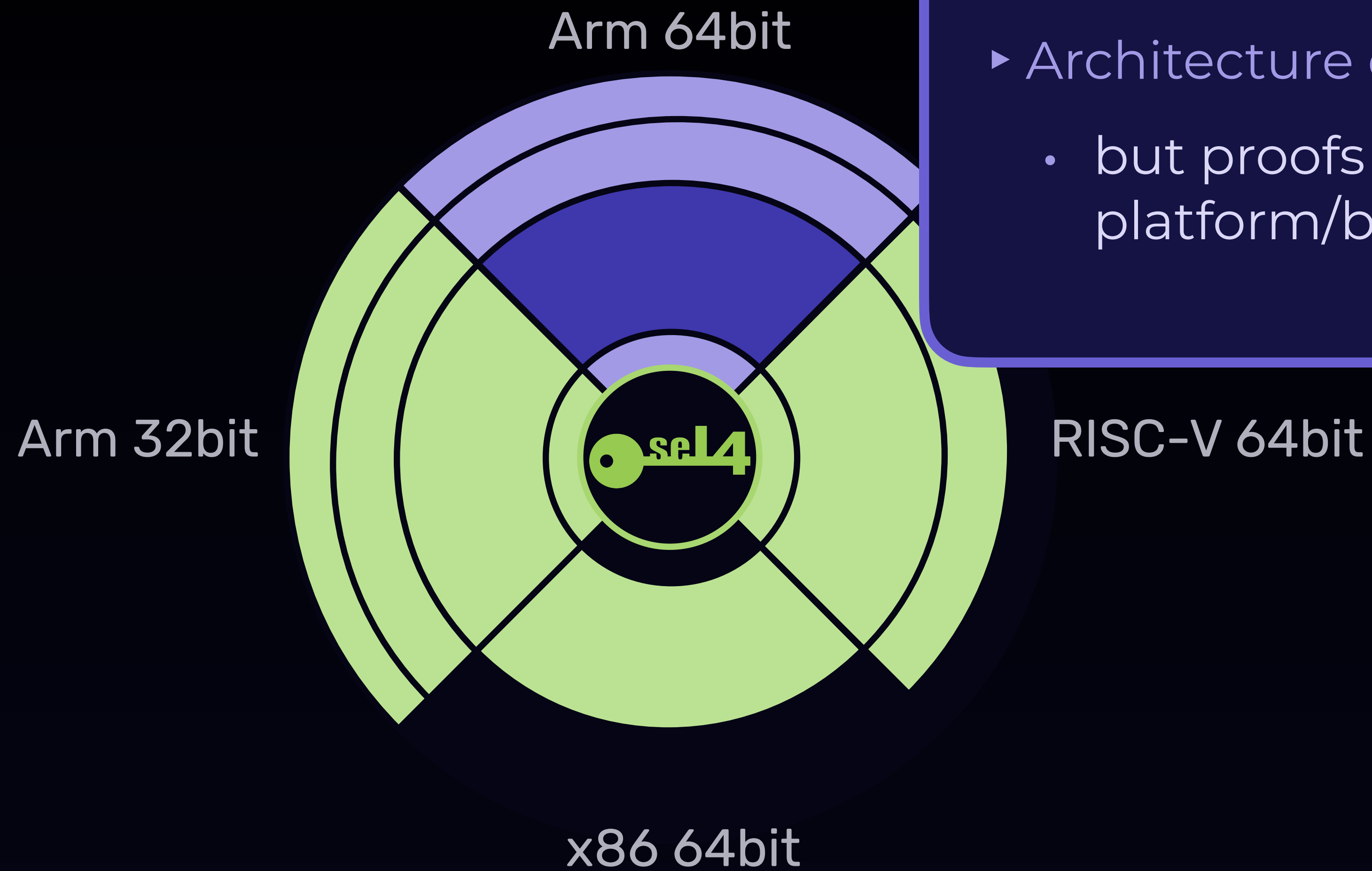
- ▶ Formal verification is great, but
 - availability of experts still a bottleneck
 - development time still hard to scale

- ▶ We can improve that
 - reduce proof maintenance cost
 - increase proof development speed
 - make specific verification tasks automatic



Automatic Verification of Platform Ports

Verified seL4 Architectures



- ▶ Architecture coverage fairly good
 - but proofs apply only to one platform/board for each architecture

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 - but proofs apply only to one platform/board for each architecture

- ▶ Platform coverage recently extended for Arm 32bit
 - 3 different verified Arm platforms: Sabre Lite, Exynos 5, IMX8MM-EVK
 - but git branches brittle in maintenance
 - not scalable to many boards

Verified seL4 Platforms (currently)



► <https://docs.sel4.systems/Hardware/>
seL4 supports 32 different platforms

Select ARMv7 and ARMv8 Platforms.

ARM Platforms

System-on-chip	Core	Arch	Virtualisation	IOMMU	Status	
Arndale	Exynos5	Cortex-A15	ARMv7A	ARM Hyp	No	Unverified
Avnet MaaXBoard	i.MX8MQ	Cortex-A53 Quad 1.5 GHz	ARMv8A	No	No	Unverified
BeagleBoard	OMAP3	Cortex-A8	ARMv7A	No	No	Unverified
BeagleBone Black / Blue	AM335x	Cortex-A8	ARMv7A	No	No	Unverified
HiKey	Kirin 620	Cortex-A53	ARMv8A	ARM HYP	No	Unverified
Imx8mm	IMX8MM-	Cortex-A53	ARMv8A,	No	No	FC,

Verified seL4 Platforms (currently)



► <https://docs.sel4.systems/Hardware/>
seL4 supports 32 different platforms

6 of these support verification
19%

Select ARMv7 and ARMv8 Platforms.

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Verified seL4 Platforms (plan)



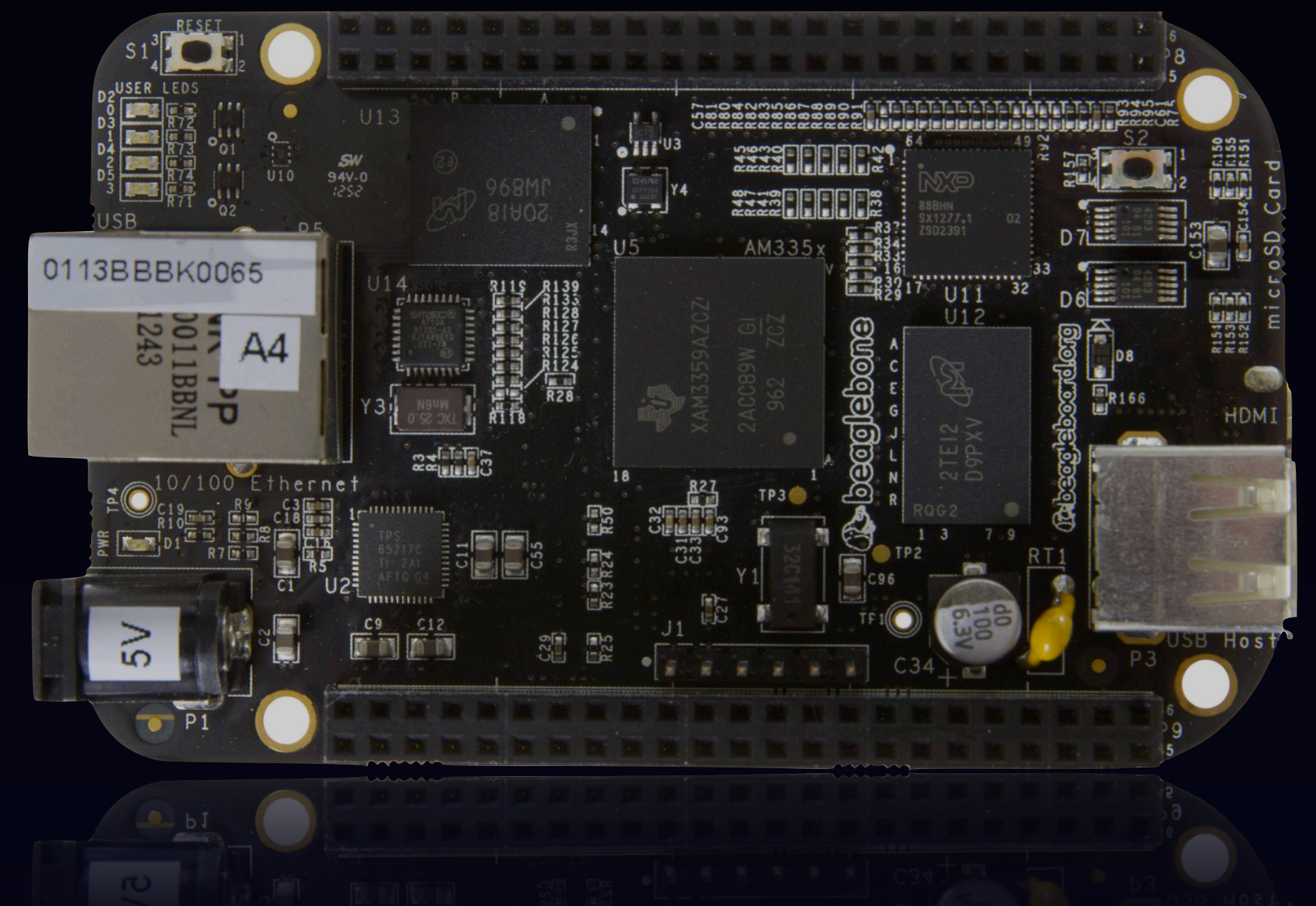
► Ideally, all platforms should have verification

► Even 90% would already be great

and ARMv8 Platforms.

Platform	Core	Arch	Virtualisation	IOMMU	Status
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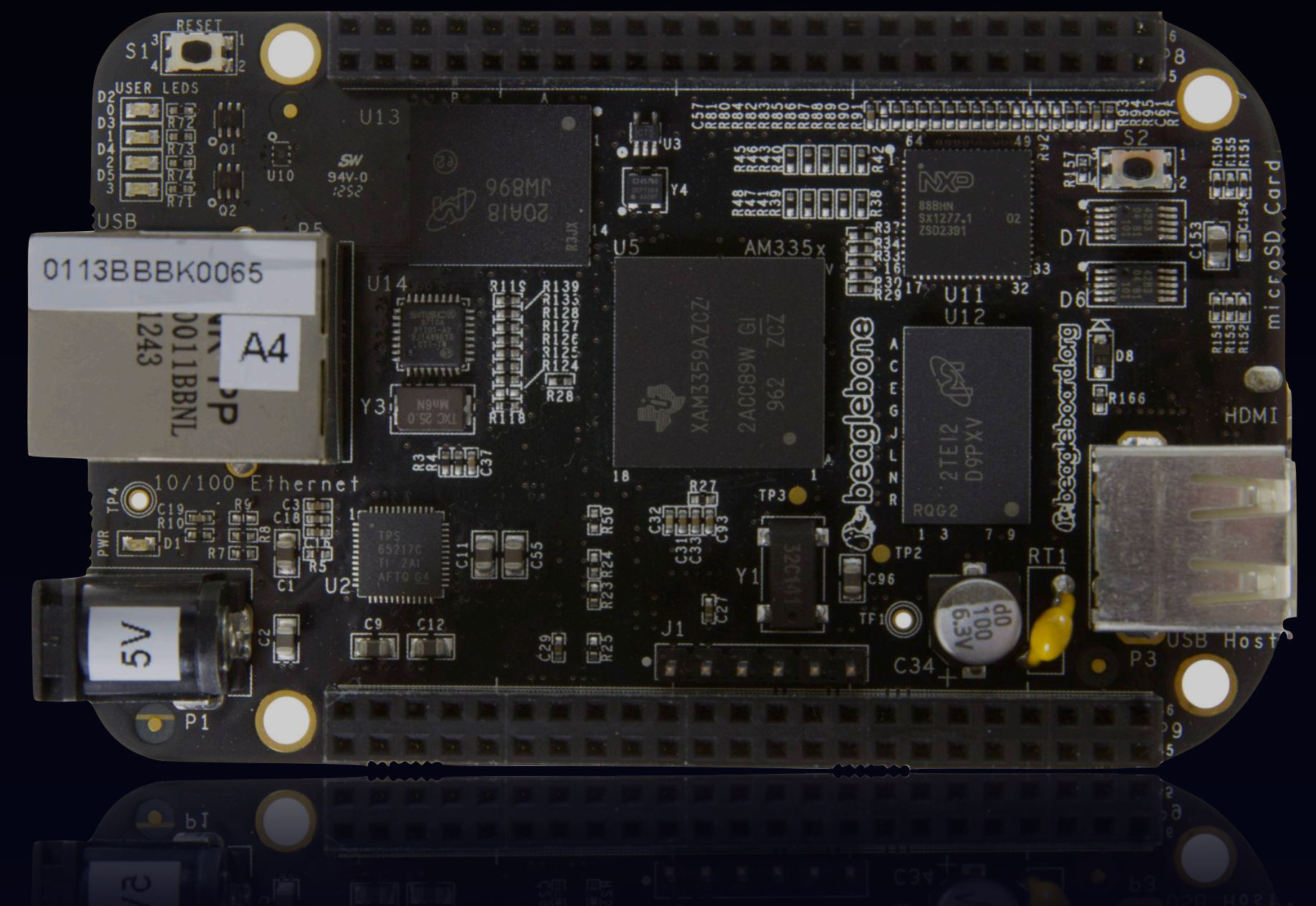
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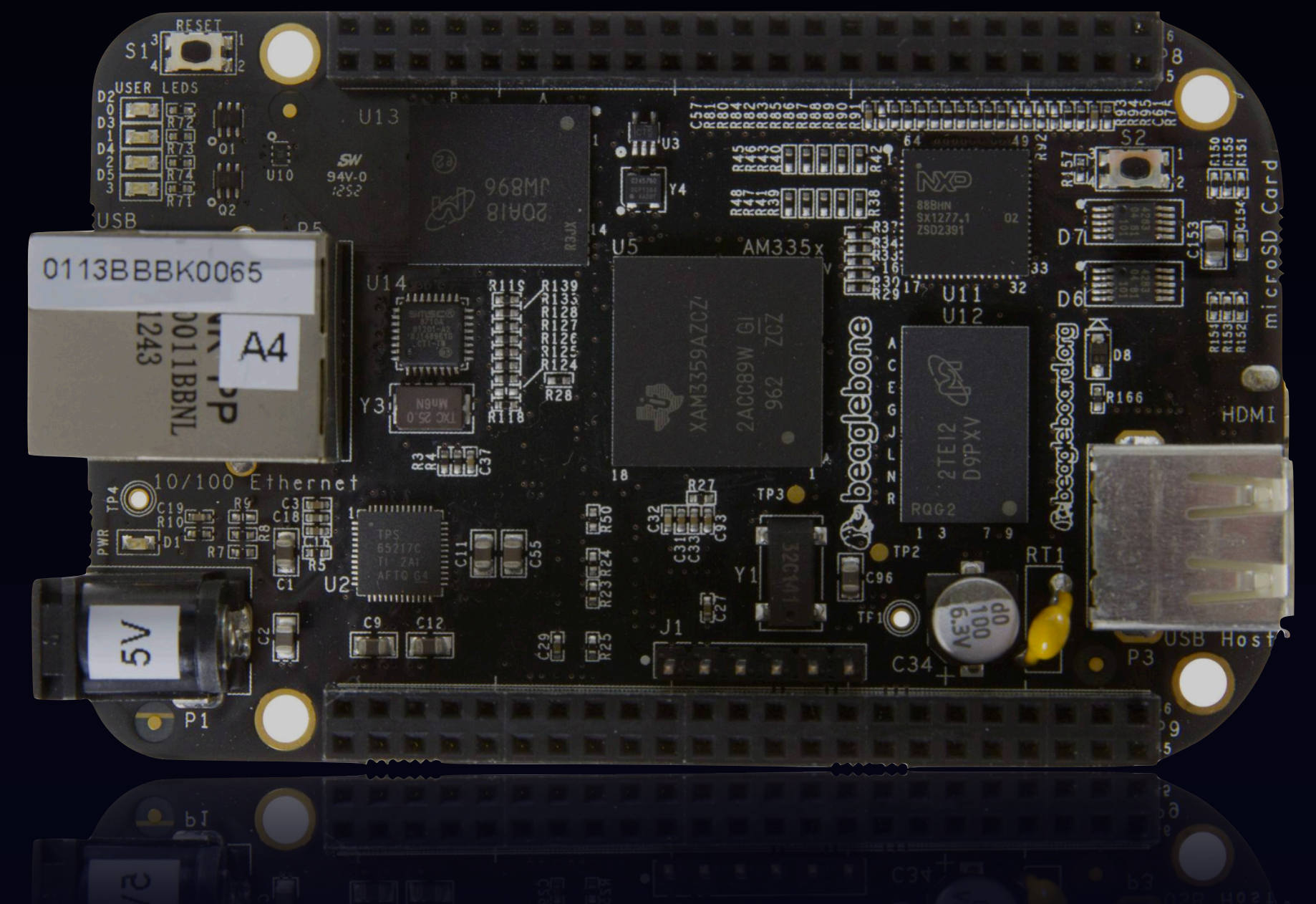
- ▶ Follow kernel porting instructions 



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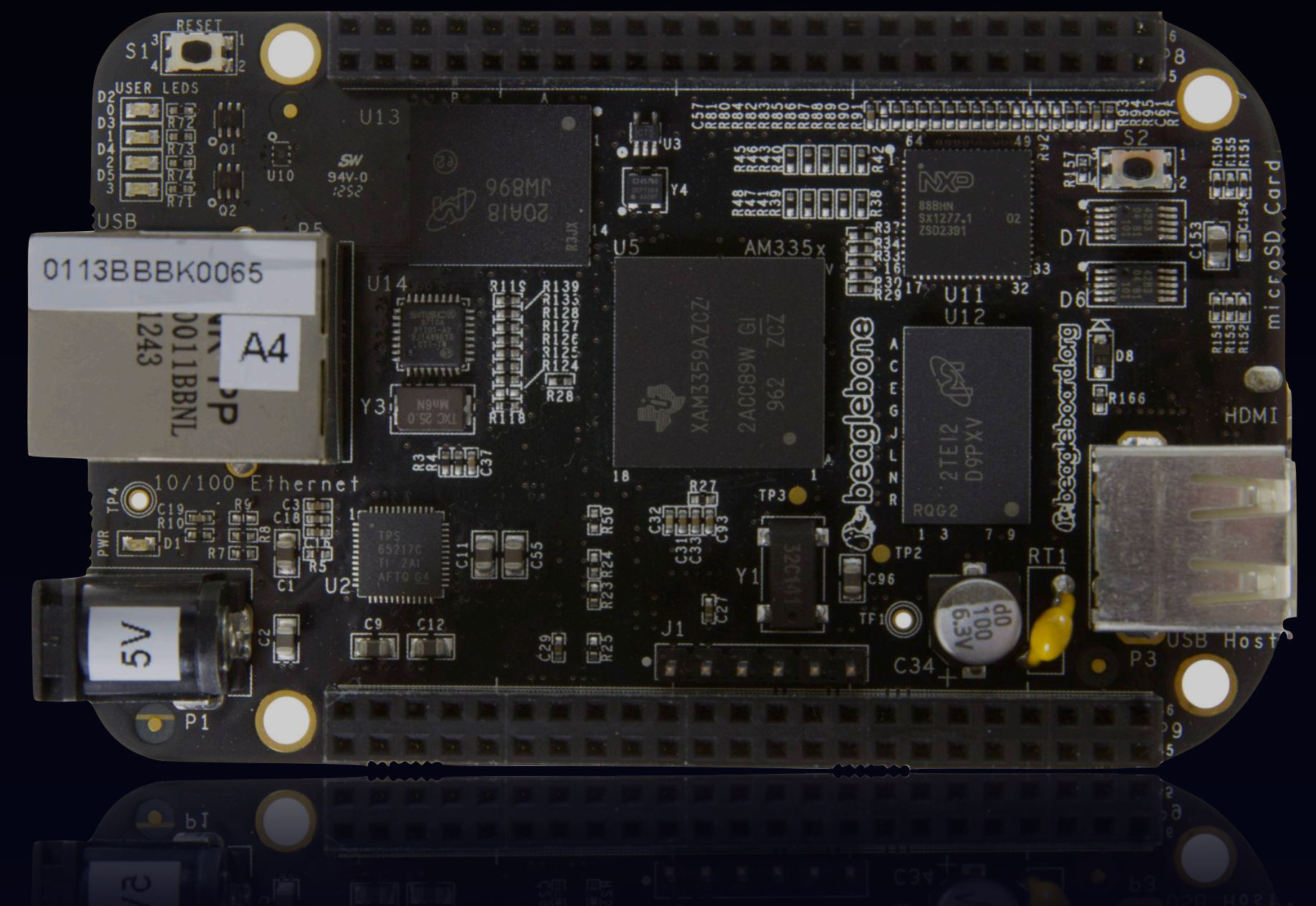
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- ▶ Now need a proof update



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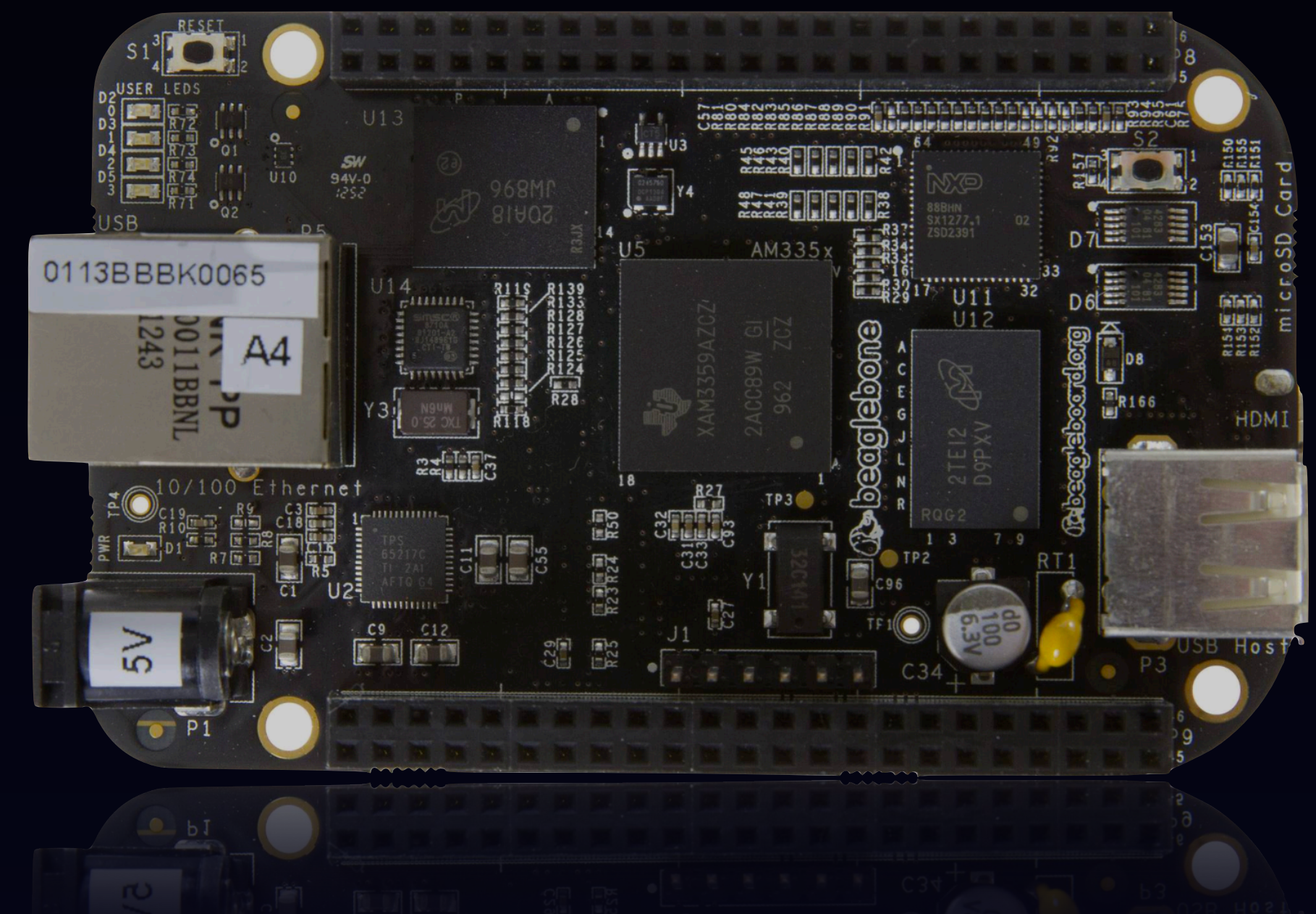
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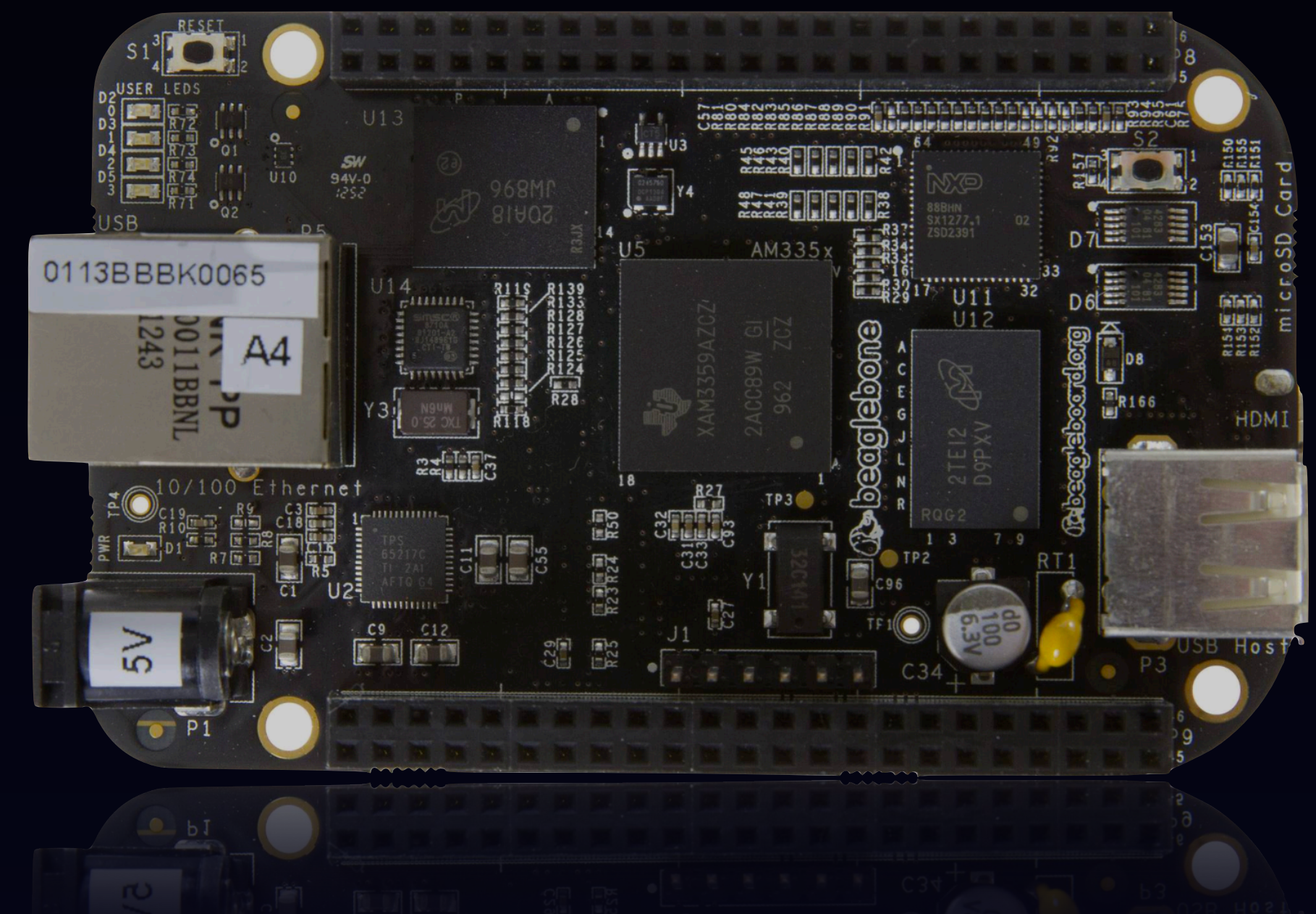
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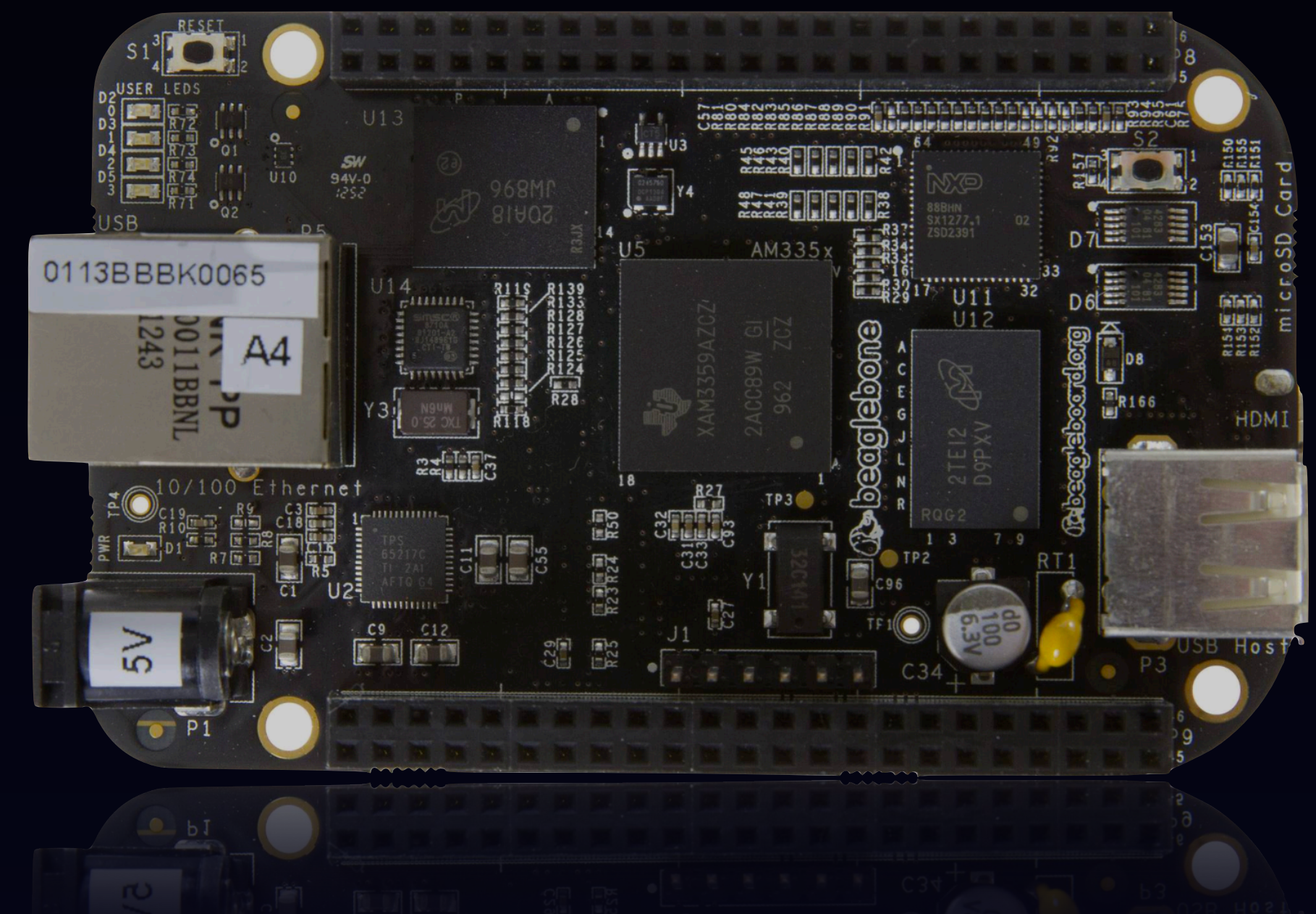
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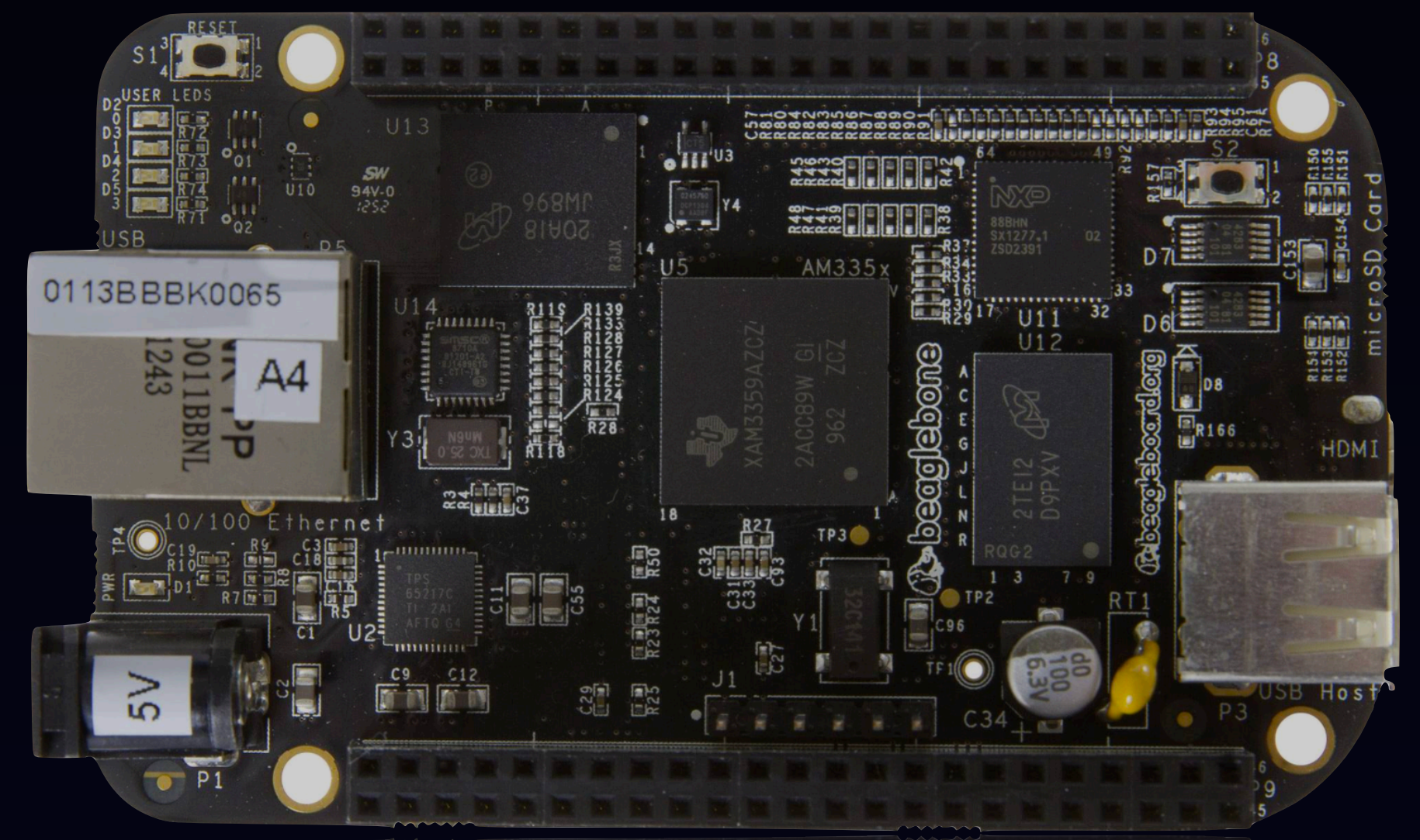
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- ▶ Proof test combinations multiply 😞



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- ▶ We perform the proof update, add a branch to the proofs. 😎
- ▶ Proof test combinations multiply 😞
- ▶ Maintenance cost increases 😞



New seL4 Platforms (currently)



- ▶ Follow kernel porting instructions ✓
- ▶ Now need a proof update
- ▶ Can contract Proofcraft ✓
- ▶ Effort between a few people
- ▶ We perform the proof
- ▶ Proof test combination
- ▶ Maintenance cost in mind
 - ▶ Update usually easy
 - ▶ Maintaining multiple proof versions is painful (> 6h run time for a single proof check)
 - ▶ When something does break, many copies to update
 - ▶ Makes maintenance boring and expensive
 - ▶ High incentive to eliminate these completely

New seL4 Platforms (plan)



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- ▶ Build automatically checks conditions and generates proof input ✓
- ▶ If you want to be really sure: re-run proof ✓
- ▶ The end 🎉

- ▶ No need for verification experts in most cases
- ▶ No new branches or maintenance explosion
- ▶ When build checks fail, there is likely a real problem that needs deeper expertise



Why and How?

What changes in a platform port



- ▶ Platform parameters
- ▶ Config parameters

```
/ {
    chosen {
        seL4,elfloader-devices =
            "serial0";
        seL4,kernel-devices =
            "serial0",
            &{/ocp/interrupt-controller@48200000},
            /* The following devices are used to s
            /* dmtimer4, OMAP Dual-Mode timer */
            &{/ocp/timer@48044000},
            &{/ocp/l4_wkup@44c00000/prcm@200000},
            &{/ocp/wdt@44e35000}; /* Watchdog time
    };
};
```

```
declare_platform(am335x KernelPlatformAM335X PLAT_AM335X KernelSel4ArchAarch32)
set(c_configs PLAT_AM335X_BONEBLACK PLAT_AM335X_BONEBLUE PLAT_AM335X_BONE)
set(
    cmake_configs
    KernelPlatformAM335XBoneBlack
    KernelPlatformAM335XBoneBlue
    KernelPlatformAM335XBone
)
set(plat_lists am335x-boneblack am335x-boneblue am335x-bone)
foreach(config IN LISTS cmake_configs)
    unset(${config} CACHE)
endforeach()

if(KernelPlatformAM335X)
    declare_sel4_arch(aarch32)

    set(KernelHardwareDebugAPIUnsupported ON CACHE INTERNAL "")

    set(KernelArmCortexA8 ON)
    set(KernelArchArmV7a ON)
    check_platform_and_fallback_to_default(KernelARMPlatform "am335x-boneblack")
    list(FIND plat_lists ${KernelARMPlatform} index)
    if("${index}" STREQUAL "-1")
        message(FATAL_ERROR "Which am335x platform not specified")
    endif()
endif()
```

What changes in a platform port



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set(
    cmake_configs
    KernelPlatformAM335XBoneBlack
    KernelPlatformAM335XBoneBlue
```

- ▶ Platform parameters:

- memory regions
- devices addresses
- board features (FPU, IRQ controller, HYP, SMMU, etc)

```
};
    &{/ocp/wdt@44e35000}; /* Watchdog time
};
```

```
oneblue am335x-bone)
ed ON CACHE INTERNAL ""|
set(KernelArmCortexA8 ON)
set(KernelArchArmV7a ON)
check_platform_and_fallback_to_default(KernelARMPlatform "am335x-boneblack")
list(FIND plat_lists ${KernelARMPlatform} index)
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```

▶ Config parameters:

- MCS on/off
- fast path on/off
- number of domains
- max retype fan-out
- init CNode size
- etc

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```
};
    &{/ocp/wdt@44e35000}; /* Watchdog time
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set(KernelA
set(KernelA
check_platf
list(FIND pla
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x-boneblack")
```

What changes in a platform port



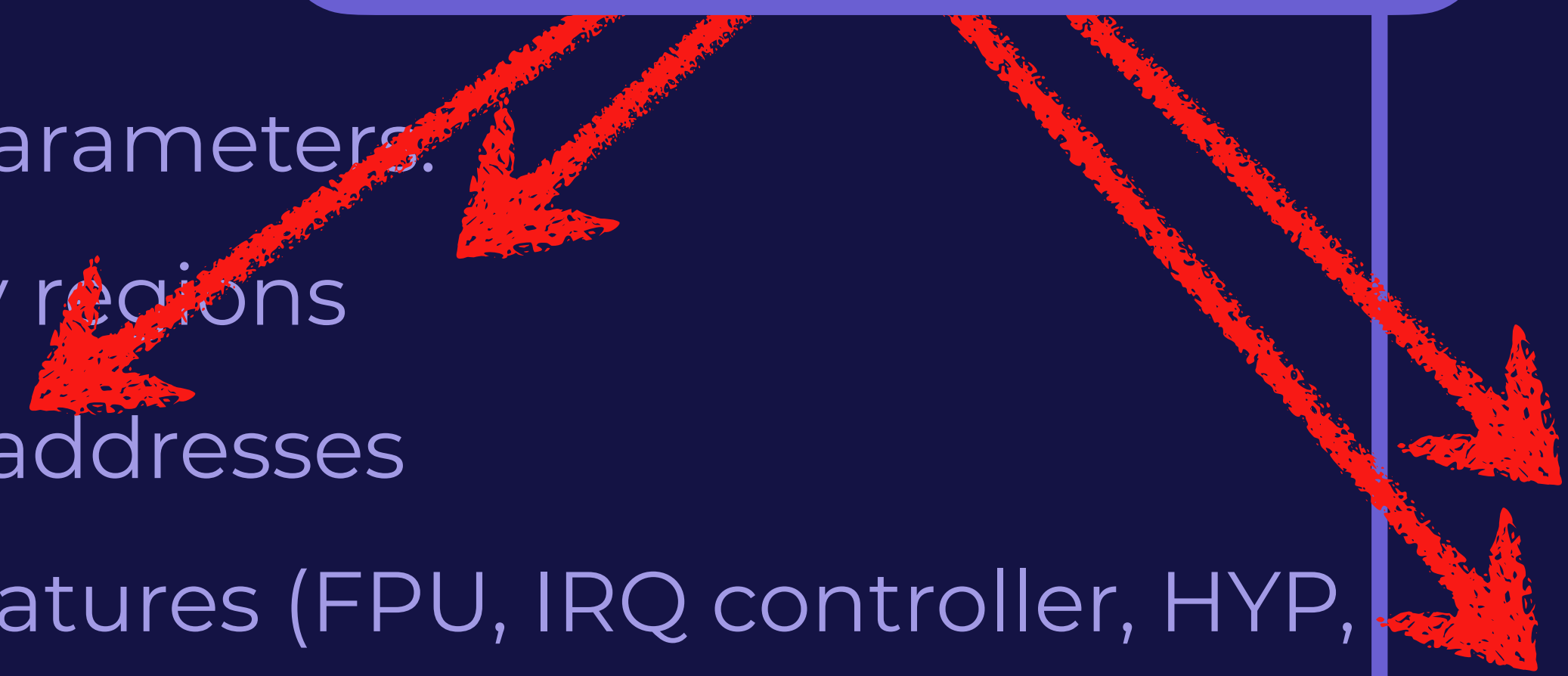
- ▶ Platform parameters
- ▶ Config parameters

Can be automated

```
335x KernelPlatformAM335X PLAT_AM335X KernelSel4ArchAarch32)  
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selects different code,
will be harder

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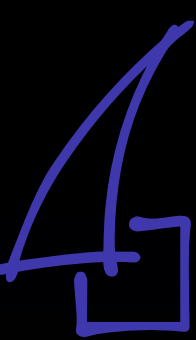
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check_p
list(FI
if("${i
mes
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```

selects different proof setup

x-boneblack")

Why is this even an issue?



Why does the proof break for these?

▶ Config parameters:

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- fast path on/off
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- init CNode size
- etc

▶ Platform parameters

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 - generate spec + config values from C config
 - validation still needs to happen manually!
 - this kind of validation usually easier with concrete testing
 - proof could be changed such that it happens to work with all reasonable values

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 - proof could be changed such that it happens to work with all reasonable values
- ▶ Not sufficient:
 - still leads to combination explosion
 - still needs you to re-run the full proofs for each change

Why is this even an issue?



▶ Current platform and config constants:

- fixed values in the spec
- tied to a specific board, validated against that board
- tied to specific config

▶ Plan:

- generate spec + config
- validation still needs to be done
- this kind of validation is tedious
- proof could be changed

▶ Not sufficient:

- still leads to combinatorial explosion
- still needs you to re-run

▶ Instead: proof parameterisation

- find sufficient conditions, e.g.:
 - physical base address must be aligned to x bits
 - must be greater than y
 - must be smaller than other config value
- prove once:
 - all values that satisfy these conditions are safe

What about FPU, GIC etc



One single proof that works with and without FPU?

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Raf says: don't promise that to people yet.

What about FPU, GIC etc



One single proof that works with and without FPU?

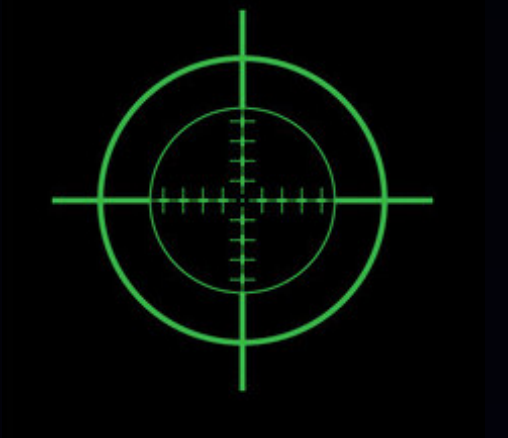
Raf says: don't promise that to people yet.

Ok, I won't, but I have a bunch of ideas, and they might work.

The goal



- ▶ Follow porting instructions
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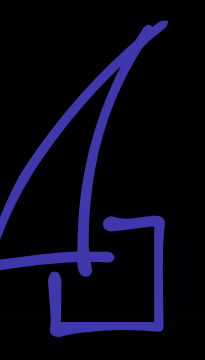


- ▶ Looks achievable for a large set of platforms and config changes
- ▶ We'll be working on it.



When can I have this?

Not the only thing we're working on..



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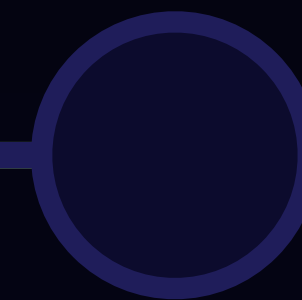
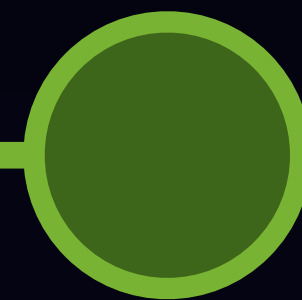


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 - AArch64 verification: functional correctness in **Mar 2024**

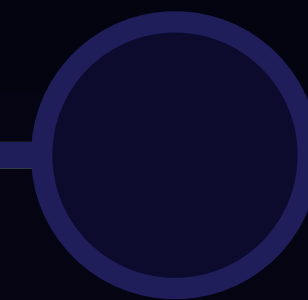
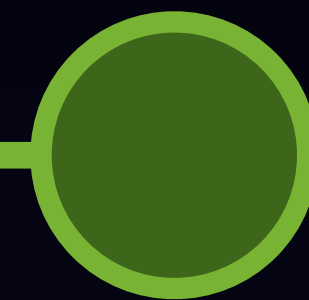


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 - **platform proof automation**
 - more generic proofs, more agility



Summary

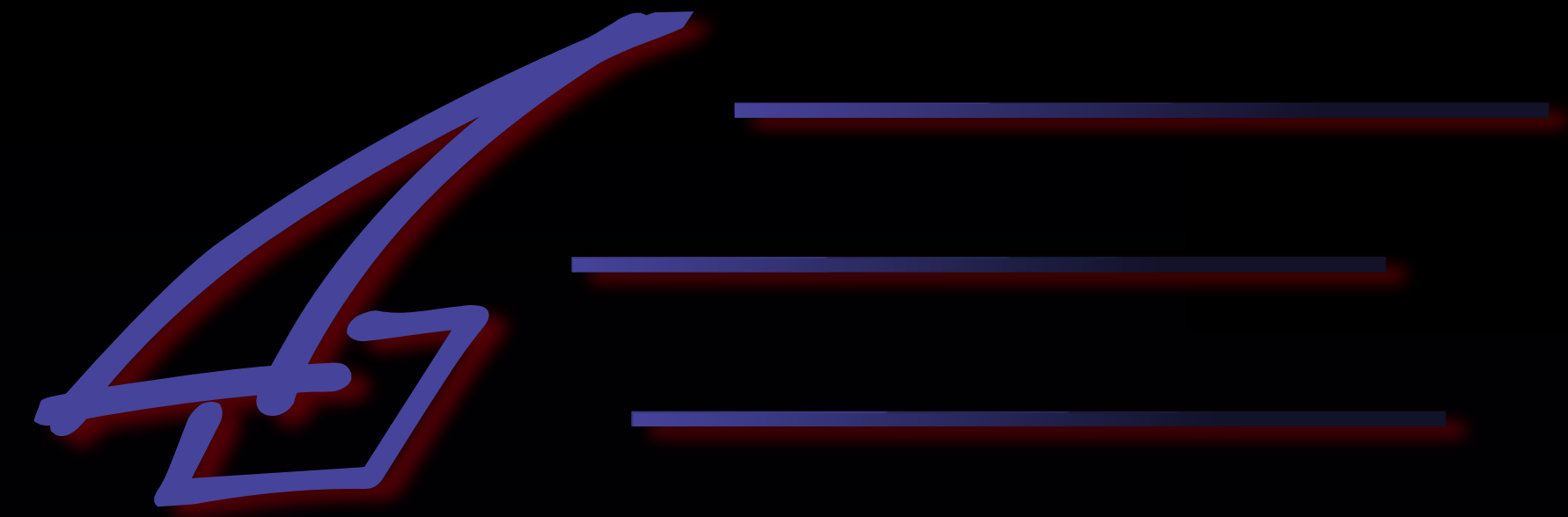
Towards more agile proofs



The Plan:

- ▶ Platform ports: no experts required
- ▶ Proof maintenance: faster and cheaper
- ▶ Proof engineers: happier and less of a bottleneck

Towards more agile proofs



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More capacity for features and updates



Thank You